

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

DAVID BISTRY, ET AL.

Application No.:

Filed:

For: **Emptying Packed Data State During
Execution of Packed Data Instructions**

Art Group: 2183

Examiner: Daniel H. Pan

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Continuation Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/785,303, filed February 16, 2001) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

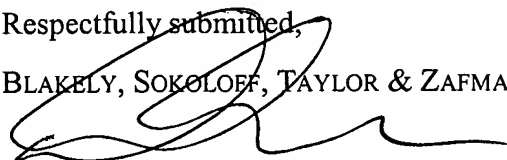
Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: _____

3/19/04

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Daniel M. DeVos, Reg. No. 37,813

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (408) 720-8300

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known		
				Application Number		
				Filing Date		
				First Named Inventor:	David Bistry	
				Art Unit	2183	
				Examiner Name	Daniel H. Pan	
Sheet	1	of	10	Attorney Docket Number		42P2737C4

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
		us-	4,707,800	3/4/1985	Montrone, et al.	
		us-	4,989,168	11/21/1988	Kuroda, et al.	
		us-	3,723,715	8/25/1982	Chen, et al.	
		us-	4,498,177	8/30/1982	Larson	
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		us-	5,760,792	5/1/1995	Holt, et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
		WO93/01543		07/08/92	PCT		
		92106065.3		08/04/92	Europe		
		93120933.2		12/27/93	Europe		
		EP 96 94 4983		9/15/99	Supplemental Partial European Search Report		
		PCT/US96/20522		4/23/1997	International Search Report		
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		US-	5,721,892	11/6/95	Peleg, et al.	
		US-	5,677,862	4/2/1996	Peleg, et al.	
		US-	5,467,473	1/8/1993	Kahle, et al.	
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		US-	5,537,606	1/31/1995	Byrne	
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		PCT/US96/20516		4/18/1997	International Search Report		
		PCT/US96/20572		3/21/1997	International Search Report		
		PCT/US96/20573		3/11/1997	International Search Report		
		PCT/US96/20516		12/15/1997	Written Opinion		
		PCT/US96/20516		4/7/1998	International Preliminary Examination Report		
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		US-	5,835,748	12/19/1995	Orenstein, et al.	
		US-	5,852,726	12/19/1995	Lin, et al.	
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		US-	5,978,901	11/2/1999	Luedtke et al.	
		US-	6,339,823	1/15/2002	Loper, Jr.	
		US-	6,412,065	6/25/2002	Loper, Jr.	
		US-	6,385,716	5/7/2002	Henry et al.	

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		PCT/US96/20572		10/17/1997	International Preliminary Examination Report		
		PCT/US96/20522		10/23/1997	Written Opinion		
		PCT/US96/20522		3/19/1998	International Preliminary Examination Report		
		PCT/US96/20573		11/19/1997	International Preliminary Examination Report		
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		US-	4,972,314	11/90	Getzinger et al.	
		US-	5,669,013	9/97	Watanabe et al.	
		US-	6,266,686	7/01	Bistry et al.	
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Sheet	5	of	10		
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		J. Shipnes, <u>Graphics Processing with the 88110 RISC Microprocessor</u> , IEEE (1992), PP 169-174.			
		<u>MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1991).			
		<u>Errata to MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1992), pp 1-11.			
		<u>MC88110 Programmer's Reference Guide</u> , Motorola Inc. (1992), p 1-4.			
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 1, 3, 8, 12.			
		R.B. Lee, <u>Accelerating Multimedia With Enhanced Microprocessors</u> , IEEE Micro (April 1995), pp 22-32			
		<u>TMS320C2x User's Guide</u> , Texas Instruments (1993) pp 3-2 through 3-11; 3-28 through 3-34; 4-1 through 4-22; 4-41; 4-103; 4-119 through 4-120; 4-122; 4-150 through 4-151.			
		L. Gwennap, <u>New PA-RISC Processor Decodes MPEG Video</u> , Microprocessor Report (Jan. 1994), pp 16, 17.			
		<u>SPARC Technology Business, UltraSPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics</u> , Sun Microsystems (Sept. 1994).			
		Y. Kawakami, et al., <u>LSI Applications: A Single-Chip Digital Signal Processor for Voiceband Applications</u> , Solid State Circuits Conference, Digest of Technical Papers; IEEE International (1980).			
		B. Case, <u>Phillips Hopes to Displace DSPs with VLIW</u> , Microprocessor Report (Dec. 1994), pp 12-15.			
		Rounce, P., et al.: ARCHITECTURES WITHIN THE ESPRIT SPAN PROJECT" IEEE Micro.			
		<u>Graphics Floating Point Engine Fix and Float Registers Consolidations</u> , IBM Technical Disclosure Bulletin, Vol. 36, No. 12, dated December 1993.			
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Sheet	6	of	10	Attorney Docket Number 42P2737C4	
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		L. Gwennap, <u>UltraSparc Adds Multimedia Instructions</u> , Microprocessor Report (Dec. 1994) pp 16-18.			
		N. Margulis, <u>i860 Microprocessor Architecture</u> , McGraw Hill, Inc. (1990), Ch. 6, 7, 8, 10, 11.			
		<u>Pentium Processor User's Manual, Volume 3; Architecture and Programming Manual</u> , Intel Corporation (1993), Ch. 1, 3, 4, 6, 8, and 18.			
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		Keith Diefendorff, et al., "Organization of the Motorola 88110 Superscalar RISC Microprocessor", IEEE Micro, pgs. 40-63, April 1992.			
		R.D. Dixon, "A Stack-Frame Architecture Language Processor", The Journal of Forth Application and Research, Proceedings of the 1987 Rochester Forth Conference, Volume 5, Number 1, pp. 11-25.			
		Linley Gwennap, "UltraSparc Adds Multimedia Instructions, Other New Instructions Handle Unaligned and Little-Endian Data", Microprocessor Report, pgs. 16-18, December 5, 1994.			
		L. Kohn, et al., "The Visual Instruction Set (VIS) in UltraSPARC™", IEEE Computer Society Press, pgs. 462-469, Los Alamitos, California, 1995.			
		Philip Koopman, Jr., "Stack Computers the new wave", Mountain View Press, pgs. 3-234, La Honda, California, 1989.			
		Centaur's Reply to Intel's Counterclaims of May 16, 2002, In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, <u>Via Technologies, Inc., a Taiwan Corporation, and Centaur Technology, Inc. vs. Intel Corporation vs. Via-Cyrix, Inc., a Texas Corporation, and Via Technologies, Inc. (U.S.)</u> , filed May 31, 2002 (12 pgs.)			
		Reply of Intel to Centaur's Counterclaim, In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, <u>Via Technologies, Inc., a Taiwan Corporation, and Centaur Technology, Inc. vs. Intel Corporation vs. Via-Cyrix, Inc., a Texas Corporation, and Via Technologies, Inc. (U.S.)</u> , filed June 13, 2002 (4 pgs)			
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		Defendants' Particulars of Objections, In the High Court of the Hong Kong Special Administrative Region (Hong Kong), Court of First Instance of NO. 4265 OF 2001, Case No. HCA 4265/2001, <u>Intel Corporation vs. Via Technologies Inc.; Trend Electronics (HK) Limited, , dated December 27, 2001 (4 pgs)</u>			
		Defence and Counterclaim, In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, <u>Intel Corporation vs. Via Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, dated December 12, 2001 (pgs. 1-13); Annex 1 to the Defence and Counterclaim (2 pgs); Annex 2 to the Defence and Counterclaim (4 pgs.); Cover page (1 pg)</u>			
		Defendants' Particulars of Objections, In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, <u>Intel Corporation vs. Via Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, dated December 12, 2001 (4 pgs)</u>			
		<u>VIA Technologies Inc., a Taiwan Corporation, and Centaur Technology, Inc., a California Corporation, v. Intel Corporation, a Delaware Corporation, "VIA's and Centaur's Short Statement Regarding Their Non-Infringement and Invalidity Defenses –Filed Under Seal (Redacted)," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, filed January 24, 2002; total pages: 14.</u>			
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc. and VIA Technologies, Inc. (US), "VIA Parties' Second Supplemental Responses to Intel's Interrogatory (No. 3) and Third Supplemental Response to Intel's Interrogatory (No. 6) (Redacted)," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated April 2, 2003; total pages: 46.</u>			
Examiner Signature				Date Considered	

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	
				Filing Date	
				First Named Inventor: David Bistry	
				Art Unit 2183	
				Examiner Name Daniel H. Pan	
Sheet	8	of	10	Attorney Docket Number 42P2737C4	
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc. and VIA Technologies, Inc. (US), "VIA Parties' Notice Under 35 U.S.C. § 282,"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, dated March 7, 2003; total pages: 21.			
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc., VIA Technologies, Inc. (US), and IP First, L.L.C., "VIA Parties' Motion For Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode – Filed Under Seal,"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 17, 2003; total pages: 106 (including tabs)			
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc., VIA Technologies, Inc. (US), and IP First, L.L.C., "Intel's Opposition To The VIA Parties' Motion for Summary Judgment of Invalidity of U.S. Patent No. 5,835,748 Based On Best Mode,"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), filed March 24, 2003; total pages: 31 (including tabs).			
		<u>VIA Technologies Inc., a Taiwan Corporation, and Centaur Technology, Inc., a California Corporation, v. Intel Corporation, a Delaware Corporation, "The VIA Parties' Reply To Intel's Opposition To The VIA Parties' Motion for Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode – Filed Under Seal,"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, dated March 27, 2003; total pages: 44 (including tabs).			
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation, "ORDER: That...VIA Parties' Motion For Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode [#356] is DENIED..., signed by Sam Sparks, United States District Judge,"</u> In the United States District Court for the Western District of Texas, Austin Division, Case No. A-01-CA-602-SS, dated March 31, 2003; total pages: 23.			
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc., VIA Technologies, Inc. (US), and IP First, L.L.C., "Expert Report of Harold Stone (2),"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 17, 2003; total pages: 89 (including tabs).			
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				Application Number	
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				First Named Inventor: David Bistry	
				Art Unit 2183	
Examiner Name Daniel H. Pan		Attorney Docket Number 42P2737C4			
Sheet 9	of 10				
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc., VIA Technologies, Inc. (US), and IP First, L.L.C., "Rebuttal Expert Report of Dr. V. Thomas Rhyne Regarding The Validity of U.S. Patent Nos. 5,701,508; 5,835,748 and 5,201,043," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 24, 2003; total pages: 73 (including tab).			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Intel's Opening Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 25 February 2003; total pages: 100.			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Intel's Closing Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 13 th February 2003; total pages: 144.			
		Intel Corporation vs. VIA Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, "Intel Corporation v. VIA Technologies, Inc., and others; Defendants' Opening Skeleton Argument – Introduction to the Proceedings (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 27 th January 2003; total pages: 99 (including tabs).			
		Intel Corporation vs. VIA Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, "Intel Corporation v. VIA Technologies, Inc., and others; Defendants' Closing Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 13 th February 2003; total pages: 70 (including tabs).			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Expert's Report of Simon Richard Jones (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 20/12/02 (December 20, 2002); total pages: 109 (including tabs).			
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				First Named Inventor: David Bistry	
				Art Unit 2183	
				Examiner Name Daniel H. Pan	
Sheet	10	of	10	Attorney Docket Number 42P2737C4	
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		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Expert Report of Michael David May (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 23 December 2002; total pages: 268 (including tabs).			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Expert Report of Michael David May: Exhibits MDM-1 to MDM-11 (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 23 December 2002; total pages: 367 (including tabs).			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Bundle X – Exhibits Produced At Trial," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dates produced: 05.02.03 – 11.02.03; total pages: 139 (including tabs).			
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Order Before the Honorable Mr. Justice Pumfrey, Monday the 3 rd day of February 2003: Claimant has leave to amend the Specification of United Kingdom Patent No. GB 2 326 494...", In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 3 rd Day of February 2003; total pages: 2.			
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